Multivariable Decoupling Controller: Application to Multicellular Converter

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Abstract—A new control strategy is presented in this paper, based on previous works limited to the control of the capacitor voltages considered as the outputs of a three cell converter. An additional control input is proposed to this latter in order to obtain the desired current output. The experimentations performed on a multicellular converter are presented and the discussed results showing the efficiency of the contribution.

Keywords—Hybride systems; Multicellular series converters; PWM; Closed loop control.

I. INTRODUCTION

Industrial applications require electric power which may vary from a few kilowatts to megawatts of power [1], [2]. In order to achieve that purpose, there is a need to control the electrical power from the source before sending it to the load, hence the appearance of multicellular serie converters that were designed in 1990. The device is a multistage serie converter used to distribute the elevated voltage of the power source by dividing it over several cells and limit constraints voltages undergone by the semiconductor components. The latter adopt a switching behavior, the semiconductors can be either closed or opened, which changes the operation mode of the converter depending on the states of the switches. Hence, the electrical power output can be adapted according to the required load. Each cell of the converter is constituted by alternatively operating the two switches (if one is open the other is closed and alternatively). The switches are considered ideal, so their behavior can be modeled by a discrete state: the open state (value = 0) and the closed state (value = 1). Therefore, this system can be considered a hybrid system [3], as it contains continuous dynamic changes related to discrete variations. In the case of multicellular converter, the importance of the control strategy comes from the need for electrical power adapted to the load, but the high speed switching of the semi conductors causes the appearance of several unwanted harmonics in the output signal. This is where the different control techniques can stand trying to eliminate this undesirable effect [4]. Other requirements may also come into play, such as the energy level performance and power losses that have repercussions on the converter topology. In [5] a binary control was developed based on a Lyapunov function where the author proposed an algorithm that limits the number of the switches commutations to extend their durability. In the control strategy proposed in [6] the authors present a nonlinear decoupling input to a three cell converter, which is able to obtain good performances in controlling the capacitors voltages without regards to the current output, in fact only its average value was controlled. This paper proposes an additional control input to stabilize the instant value of the current output.

The following section start by introducing the system. Section three presents a nonlinear decoupling input control. Section forth describe the control schemes used on an experimental setup. Section fifth will conclude this paper.

II. MATHEMATICAL MODELING

Multicellular converter series are the association of several cells separated by capacitors. Each cell is formed by two complementary switches. The voltages across each cell must be kept well-defined to ensure the proper functioning of the entire system values. The following structures can be DC-DC converters (chopper operation), or as DC-AC (inverter operation) [5]. In general, the structure for p cells multicellular, Fig.1, have (p-1) capacitors, 2p operating modes and (p+1) voltage levels at the terminals of the capacitors.

In the case of a three cell converter there is eight operating modes. The following table gives the different configurations depending on the state of the switches (S_3, S_2, S_1) and the corresponding output voltages.

For modeling a multi-converter, we have two models:

1) The Exact Model or Snapshot: This model is only used to validate the controls. It is accurate because it takes exactly the switch status (On or Off) at any time. I_{ck} is the current through the capacitor and the signal $S_k = 0, 1$ are commands

	S3	S2	S1	Vout
Mode 0	0	0	0	0
Mode 1	0	0	1	E/3
Mode 2	0	1	0	E/3
Mode 3	0	1	1	2E/3
Mode 4	1	0	0	E/3
Mode 5	1	0	1	2E/3
Mode 6	1	1	0	2E/3
Mode 7	1	1	1	E

TABLE I: MODE CONFIGURATION OF A THREE CELLS CONVERTER



Fig. 1: Structure of a converter associated with a RL circuit as a load

of the switches (k = 1,2,3).

The current as a function of the control signals is of the form:

$$I_{ck} = (S_{k+1} - S_k)I_{out} \tag{1}$$

Also it can have the next form depending on the voltage across the capacitors:

$$I_{c_k} = C_k \left(\frac{dV_{c_k}}{dt}\right) \tag{2}$$

Combining the two equations (1) and (2) gives:

$$\left(\frac{dV_{c_k}}{dt}\right) = \left(\frac{S_{k+1} - S_k}{C_k}\right)I_{out} \tag{3}$$

2) Equations of the tensions:

$$\frac{dV_{c_1}}{dt} = (\frac{S_2 - S_1}{C_1})I_{out}$$
(4)

$$\frac{dV_{c_2}}{dt} = (\frac{S_3 - S_2}{C_2})I_{out}$$
(5)

3) Equations of the currents:

$$I_{c_1} = C_1(\frac{dV_{c_1}}{dt})$$
(6)

$$I_{c_2} = C_2(\frac{dV_{c_2}}{dt})$$
(7)

According to the law of the meshes, the voltage Vs is the sum of the voltages across the switches:

$$V_{out} = \sum_{k=1}^{p} (V_{c_k} - V_{c_{k-1}}) S_k \tag{8}$$

Where $V_{c_0} = 0V$ and $V_{c_p} = E$. The evolution of the current in the load is given by:

$$\frac{dI_{out}}{dt} = \frac{V_{out}}{L} - \frac{R}{L}I_c \tag{9}$$

By substituting (8) we obtain:

$$\frac{dI_{out}}{dt} = -\frac{R}{L}I_c - (\frac{S_2 - S_1}{L})V_{c_1} - (\frac{S_3 - S_2}{L})V_{c_2} + \frac{E}{L}S_3$$
(10)

The state space model can be written as follows:

$$\begin{pmatrix} \frac{dV_{c_1}}{dt} \\ \frac{dV_{c_2}}{dt} \\ \frac{dI_{out}}{dt} \end{pmatrix} = \begin{pmatrix} 0 & 0 & \frac{S_2 - S_1}{C_1} \\ 0 & 0 & \frac{S_3 - S_2}{C_2} \\ \frac{S_1 - S_2}{L} & \frac{S_2 - S_3}{L} & -\frac{R}{L} \end{pmatrix} \begin{pmatrix} V_{c_1} \\ V_{c_2} \\ I_{out} \end{pmatrix} + \begin{pmatrix} 0 \\ 0 \\ \frac{E}{L}S_3 \end{pmatrix}$$
(11)

An open loop control strategy was proposed in [7], were the control input is able to lead the system outputs to theirs corresponding references. The switches are controlled by pulses generated by the intersection between a triangular carrier signal and modulating, Fig. 2. The triangular signals are described by the following equations:

$$\begin{cases} f_1(u) = (\arcsin(\sin(2\pi f_p t - \varphi) + \frac{\pi}{2})/\pi \\ f_2(u) = (\arcsin(\sin(2\pi f_p t - \varphi - \delta) + \frac{\pi}{2})/\pi \\ \cdot \\ \cdot \\ f_p(u) = (\arcsin(\sin(2\pi f_p t - \varphi - (p-1)\delta) + \frac{\pi}{2})/\pi \end{cases}$$
(12)

The phase shift δ is equal to $2\pi/p$ which would reduce the harmonics of the output signal.



Fig. 2: PWM control strategy

The mentioned open loop control strategy generates several oscillations in the system outputs around their references. Also, the response of the system is very slow.

A. The average values Model

Let consider the system described in Fig. 2, the average model is based on the Fourrier series decomposition control signals. It allows a dynamic representation of the multicellular converter. It is assumed that the transitional regime is negligible. In this context, we can decompose in the Fourier series form each control function $S_k(t)$ as following [5]:

$$S_k(t) = a_0 + \sum_{p=1}^{+\infty} (a_p \cos(p\frac{2\pi}{T_D}t) + b_p \sin(p\frac{2\pi}{T_D}t))$$
(13)

with $a_0 = \frac{1}{T_D} \int_0^{T_D} f(t) dt$ and $f(t) = \begin{cases} 1 \text{ for } t < t_\alpha \\ 0 \text{ for } t_\alpha < t < T_D \end{cases}$ thus $a_0 = \frac{1}{T_D} \int_0^{t_\alpha} dt = \frac{t_\alpha}{T_D}$ Having the duty cycle $\alpha = \frac{t_\alpha}{T_D}$ leads to $a_0 = \alpha$

In addition, the function $S_k(t)$ is pair, so $b_p = 0$ and

$$a_p = \frac{2}{T_D} \int_0^{T_D} f(t) \cos(p \frac{2\pi}{T_D} t) dt$$
$$= \frac{2}{T_D} \int_0^{\alpha_k T_D} \cos(p \frac{2\pi}{T_D} t) dt = u_{kp}(\alpha_k)$$

Hence, the Fourier series decomposition of the control function has the following expression:

$$S_{k}(t) = \alpha_{k} + \sum_{p=1}^{+\infty} (u_{kp}(\alpha_{k})\cos(p\frac{2\pi}{T_{D}}t))$$
(14)

When passing to the average value equation (14) becomes $\langle S_k(t) \rangle = \alpha_k$.

Each control variable is replaced by its average value over a period T_D , so the exact model described by equations (6), (7) and (8) becomes:

$$\begin{cases} < I_{c1} >= (\alpha_2 - \alpha_1)I_{out} \\ < I_{c2} >= (\alpha_3 - \alpha_2)I_{out} \\ < V_{out} >= \alpha_3 E + V_{c1}(\alpha_1 - \alpha_2) + V_{c2}(\alpha_2 - \alpha_3) \end{cases}$$
(15)

It uses the duty ratio as information for the switching cell.

Assuming floating voltages do not vary very much on the chopping period [7], we obtain: $V_{ck_{moy}}(t) \approx V_{ck}(t)$ which leads to the following equations:

$$\begin{cases} \frac{dV_{c_k}}{dt} = \frac{(\alpha_{k+1} - \alpha_k)}{c_k} I_{out} \\ V_{out_{moy}} = \sum_{k=1}^p (V_{c_k} - V_{c_{k-1}}) \alpha_k \end{cases}$$
(16)

So the average values model of a three cells converter will be as follows:

$$\begin{pmatrix} \frac{dV_{c_1}}{dt} \\ \frac{dV_{c_2}}{dt} \\ \frac{dI_{out}}{dt} \end{pmatrix} = \begin{pmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & -\frac{R}{L} \end{pmatrix} \begin{pmatrix} V_{c_1} \\ V_{c_2} \\ I_{out} \end{pmatrix} + \begin{pmatrix} -\frac{I_{out}}{c_1} & \frac{I_{out}}{c_1} & 0 \\ 0 & -\frac{I_{out}}{c_2} & \frac{I_{out}}{c_2} \\ -\frac{V_{c_1}}{L} & -\frac{V_{c_2}}{L} & \frac{E}{L} \end{pmatrix} \begin{pmatrix} \alpha_1 \\ \alpha_2 \\ \alpha_3 \end{pmatrix}$$
(17)



Fig. 3: The experimental setup

III. MATERIALS

The platform used to validate the next control technique is a system constituted of three legs initially connected to a three phase motor and replaced by a R-L load $(R = 20\Omega, L = 0.01mH)$ for this application. We used a three-cell converter, see Fig. 3. The value of the capacitors between the cells are $C_1 = C_2 = C = 40\mu F$. The switches are bipolar transistors (IGBTs). Current and voltage measurements are obtained from voltage sensors and current transductors. The voltage source of the converter is E = 60V. A Matlab program script implementing the predictive algorithm was compiled and run by a DSPACE card. The card is used to control switch gates using its binary outputs, and receives measurements of the current and the two capacitor voltages through input card.

IV. EXPERIMENTAL RESULTS OF PWM CONTROL

The PWM (Pulse Width Modulation) control is a standard technique which can be used in case having a prime number of the cells in the converter [5]. It allows the control of the output current while keeping the voltages across the capacitors around the reference values.

We proceed with having: $\beta_3 = \alpha_3 - \alpha_1$ and $\beta_2 = \alpha_2 - \alpha_1$ we obtain:

$$\begin{pmatrix} C_1 \frac{dV_{c_1}}{dt} \\ C_2 \frac{dV_{c_2}}{dt} \end{pmatrix} = \begin{pmatrix} 1 & 0 \\ -1 & 1 \end{pmatrix} \begin{pmatrix} \beta_2 \\ \beta_3 \end{pmatrix} I_{out}$$
(18)

And therefore:

$$\binom{\beta_2}{\beta_3} = \frac{1}{I_{out}} \begin{pmatrix} 1 & 0\\ -1 & 1 \end{pmatrix}^{-1} \begin{pmatrix} C_1 \frac{dV_{c_1}}{dt} \\ C_2 \frac{dV_{c_2}}{dt} \end{pmatrix}$$
(19)

If $c_1 \frac{dV_{c_1}}{dt}$ and $c_2 \frac{dV_{c_2}}{dt}$ are replaced by γ_2 and γ_3 we obtain

$$\begin{cases} \beta_2 = \frac{\gamma_2}{I_{out}} \\ \beta_3 = \frac{\gamma_3 + \gamma_2}{I_{out}} \end{cases}$$
(20)

Equation (20) is replaced in (15) leading to:

$$\begin{cases} < I_{c1} >= c_1 \frac{dV_{c_1}}{dt} = \beta_2 I_{out} = \gamma_2 \\ < I_{c2} >= c_2 \frac{dV_{c_2}}{dt} = (\beta_3 - \beta_2) I_{out} = \gamma_3 \\ < V_{out} >= (\alpha_1 + \frac{\gamma_2}{I_{out}} + \frac{\gamma_3}{I_{out}}) E - \frac{\gamma_2}{I_{out}} V_{c1} - \frac{\gamma_3}{I_{out}} V_{c2} \end{cases}$$
(21)

From the system equations (21) we can see that $\frac{dV_{c_1}}{dt}$ and $\frac{dV_{c_2}}{dt}$ are decoupled but $\langle V_{out} \rangle$ remain coupled. So, another decoupling control input is added to control the average value of the current output.

Suppose that $\langle V_{out} \rangle = V_{ref} = RI_{ref}$, from equation (15) a decoupling control input can be as follows:

$$\alpha_1 = \frac{1}{V_{c1}} \left[V_{ref} - \alpha_3 (E - V_{c2}) + \alpha_2 (V_{c2} - V_{c1}) \right]$$
(22)

The principle is illustrated in the following figure 4 for a threecell converter. Two controllers (PI) are assigned for regulating V_{c1} and V_{c2} .

In [5], the control strategy is able to drive the output volt-



Fig. 4: Closed loop control strategy applied to a three-cell converter

ages to their desired values but the current response presents several undulations around its set-point as shown in Fig. 5. The reason is related to the average model used in the control input (22) which lead obviously to control only the average value of the current output and not its instantaneous value, thus it allows the appearance of several oscillations around its reference.

To solve this problem, we associate another control term γ_1 that will be the output of a PI controller, the idea is inspired from [8] were the author combine a PID control action with a nonlinear control law. Thus the final expression controlling the current output will be the sum of a decoupling action and a PI control action.

$$\alpha_1 = \frac{1}{V_{c1}} \left[RI_{ref} - \alpha_3 (E - V_{c2}) + \alpha_2 (V_{c2} - V_{c1}) + \gamma_1 \right]$$
(23)

Which leads to a new structure of the overall control strategy (figure 6). An experimental results of the presented control scheme is conducted, where the frequency of the triangular signals $f_p = 1800Hz$ and the output references are as follows: $V_{c1_{ref}} = \frac{E}{3}$, $V_{c2_{ref}} = 2\frac{E}{3}$ and $I_{ref} = 1.5A$. The results are shown in Fig. 7, where it is shown that all the outputs follow theirs desired values.



Fig. 5: Experimental results of the voltages across the capacitors (V_{c1} and V_{c2}) and the current output (I_{out}) evolution



Fig. 6: The new proposed closed loop control strategy applied to a three-cell converter

V. CONCLUSION

Multicell converters are widely used in industrial applications because they offer the solution to control the electric energy between the source and the load. But, because of the discrete dynamic behavior of the semiconductors components, the system outputs present adverse effects (harmonics) that is possible to attenuate through an optimal control. The proposed control presented in this paper is based on an average model of the system, were after decoupling the system outputs from their inputs, we proceed to control the capacitor voltages. But, a problem arises in the current output were the decoupling control is not enough to attain good performances. In order to solve this problem, we added a PI control to the control input leading to acceptable performances.



Fig. 7: Experimental results of the voltages across the capacitors (V_{c1} and V_{c2}) and the current output (I_{out}) evolution

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